

## AMENDMENTS TO THE SPECIFICATION

Please replace the paragraph on page 2, lines 9-14 with the following paragraph, marked up to show changes made.

SMPs can be much faster, but at higher cost, and cannot practically be built to contain more than a modest number of CPUs, e.g, a few tens. Distributed memory systems can be cheaper, and scaled arbitrarily, but the program performance can be severely limited by the performance of the interconnect employed, since it (for example, Ethernet) can be several orders of magnitude slower than access to local ~~memory~~ memory.

Please replace the paragraph on page 7, lines 28-33 with the following paragraph, marked up to show changes made.

When a CPU desires to access a memory address, the desired memory address is presented to the VM manager 404. The VM manager 404 checks to see if it has a translation for this memory address into a physical address. If the VM manager 404 does not have a translation for this memory address, then it interrupts the CPU and causes the VM fault ~~handler~~ handler 406 to be executed. The VM fault handler 406 determines a resolution for the page fault.